Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended):

A network switch having a hybrid switch architecture, comprising:

at least two shared-memory switch fabrics, each shared-memory switch fabric being configured to store and retrieve packets; and

at least two crossbar switch fabrics, each crossbar switch fabric including

a plurality of ports for receiving packets from and transmitting packets to a plurality of network connections, wherein the plurality of ports for each crossbar switch fabric ~~is~~ are unique to that crossbar switch fabric, and

at least two channels, each of the channels coupled to one of the shared-memory switch fabrics such that each crossbar switch fabric is coupled to every shared-memory switch fabric,

each crossbar switch fabric configured to couple any one of the ports to any one of the channels to distribute packets from one of the network connections to any one of the shared-memory switch fabrics and to re-collect packets from any one of the shared-memory switch fabrics to one of the network connections.

2. (previously presented):

The network switch of claim 1, wherein each shared-memory switch fabric is a NxN shared-memory switch fabric, N being an integer greater than 1, and wherein each shared-memory switch fabric includes N inputs for receiving packets and N outputs for sending packets on N channels and wherein at least one channel of each shared-memory switch fabric is coupled to one of the channels of each crossbar switch fabric.

3. (cancelled)

4. (previously presented):

The network switch of claim 1, wherein each crossbar switch fabric includes a number of channels that is an integer multiple of a total number of shared-memory switch fabrics, the integer multiple being at least a factor of two.

5. (original):

The network switch of claim 4, comprising:

a first and second 48x48 shared memory switch fabrics; and

12 8x8 crossbar switch fabrics, each 8x8 crossbar switch fabric is coupled with 4 channels of the first and second 48x48 shared-memory switch fabrics.

6. (previously presented):

The network switch of claim 1, wherein the aggregate data rate on the channels of each crossbar switch fabric is greater than the aggregate data rate on the plurality of ports of the crossbar switch fabric.

7. (previously presented):

The network switch of claim 1, wherein the connectivity for the shared-memory switch fabrics is greater than the connectivity of the crossbar switch fabrics.

8. (original):

The network switch of claim 1, wherein each crossbar switch fabric is a 1xm crossbar switch fabric, m being an integer greater than one, and wherein each 1xm crossbar switch fabric includes 1 port for receiving and transmitting packets from and to a single network port and m channels for distributing and re-collecting packets to and from the shared-memory switch fabrics.

9. (original):

The network switch of claim 8, wherein m is an integer multiple of a total number of shared-memory switch fabrics.

10. (original):

The network switch of claim 9, comprising:

a first and second 48x48 shared-memory switch fabrics; and

12 1x8 crossbar switch fabrics, each 1x8 crossbar switch fabric is coupled with 4 channels of the first and second 48x48 shared-memory switch fabrics.

11. (previously presented):

The network switch of claim 1, further comprising:

a port controller coupled to the plurality of ports of one of the at least two crossbar switch fabrics and to at least one of the plurality of network connections, the port controller configured to retrieve packets from the at least one network connection and to forward packets to any one of the plurality of ports of the crossbar switch fabrics and configured to receive packets from any one of the plurality of ports of the one of the at least two crossbar switch fabrics and to forward packets to a destination network component via the at least one of the plurality of network connections.

12. (previously presented):

The network switch of claim 11, further comprising:

a notify ring coupled to each of a plurality of port controllers, the notify ring configured to transfer forwarding information from a first of the plurality of port controllers to a second of the plurality of port controllers, and wherein the forwarding information is used to request packets from the shared-memory switch fabrics by one of the port controllers.

13. (previously presented):

The network switch of claim 11, wherein each crossbar switch fabric is configured to distribute packets from one of the plurality of ports of the crossbar switch fabrics to more than one of the shared-memory switch fabrics without reference to the final port destination of the packets.

14. (previously presented):

The network switch of claim 13, wherein each shared-memory switch fabric is configured to store and retrieve the distributed packets from the crossbar switch fabrics in a shared buffer memory.

15. (original):

The network switch of claim 12, wherein each shared-memory switch fabric is also configured to send a packet buffer number indicating where a packet is stored in a shared buffer memory.

16. (previously presented):

The network switch of claim 15, wherein each port controller is also configured to generate the forwarding information based on the packet buffer number and a switch instance sent from each shared-memory switch fabric.

17. (original):

The network switch of claim 16, wherein each port controller is configured to request packets from each of the shared-memory switch fabrics using the forwarding information.

18. (original):

The network switch of claim 15, wherein packets are requested from each of the shared-memory switch fabrics based on an availability of a channel, and wherein the packets are capable of being requested in an order different from an order the packets were received by the crossbar switch fabrics.

19. (original):

The network switch of claim 18, wherein each crossbar switch on an egress path re-collects the requested packets and transmits the packets on egress ports in the order the requested packets were received by the crossbar switch on an ingress path before distribution.

20. (original):

The network switch of claim 18, wherein re-collected packets are stored in egress buffers, the re-collected packets are capable of being re-ordered in the egress buffers.

21. (original):

The network switch of claim 20, wherein each port controller includes:

an egress request queue storing requests to re-collect packets from the shared-memory switch fabrics, and wherein the requests are serviced based on an availability of a channel.

22. (original):

The network switch of claim 20, wherein each crossbar switch fabric further includes:

an ingress switching unit configured to receive packets and forward the received packets to channels coupled with the shared-memory switch fabrics; and

an egress switching unit configured to receive requested packets from the shared-memory switch fabrics and forward the requested packets to a port controller.

23. (original):

The network switch of claim 1, wherein the packets are data packets for an Ethernet network.

24. (original):

The network switch of claim 1, wherein the packets are data cells for an asynchronous transfer mode (ATM) network or for storage area network frames.

25-28. (cancelled)

29. (currently amended):

A method of using a network switch having a hybrid switch architecture, the method comprising:

distributing packets received from a network connection by one of a plurality of ports of a crossbar switch fabric to at least two shared-memory switch fabrics; ~~and~~

storing the packets distributed from the crossbar switch fabric in a shared buffer memory associated with each shared-memory switch ~~fabric.~~ fabric; and

sending a packet buffer number and a switch instance for each packet stored by each shared-memory switch fabric to an ingress port controller, the packet buffer number including information indicating where the packet is stored in the shared buffer memory and the switch instance including information indicating which shared-memory switch fabric stored the packet.

30. (original):

The method of claim 29, further comprising:

removing header or control information from received packets before distribution.

31. (previously presented):

The method of claim 29, wherein distributing packets distributes packets from one of the plurality of ports of the crossbar switch fabrics to more than one of the shared-memory switch fabrics is without reference to the final port destination of the packets.

32. (cancelled)

33. (currently amended):

The method of claim ~~32,~~ 29, further comprising:

generating forwarding information using the packet buffer number and the switch instance; and

sending the forwarding information to an egress port controller via a notify ring.

34. (previously presented):

The method of claim 33, further comprising:

requesting packets from the shared-memory switch fabrics by an egress port controller using the forwarding information from the ingress port controller; and

re-collecting the requested packets from the shared-memory switch fabrics by the egress port controller.

35. (original):

The method of claim 34, further comprising:

retrieving the requested packets from the shared buffer memory by the shared-memory switch fabrics; and

transmitting the packets to a destination network component in an order the packets were received by the ingress port controller.

36. (previously presented):

The method of claim 30, further comprising:

requesting packets from the shared-memory switch fabrics by an egress port controller based on an availability of a channel regardless of an order the packets were received by an ingress port controller; and

re-collecting the requested packets by the egress port controller; and

re-ordering the re-collected packets such that packets are to be transmitted to a destination network component in an order the packets were received by the ingress port controller.

37.-80. (cancelled)

81. (new):

A network switch having a hybrid switch architecture, comprising:

means for distributing packets received from a network connection by one of a plurality of ports of a crossbar switch fabric to at least two shared-memory switch fabrics;

means for storing the packets distributed from the crossbar switch fabric in a shared buffer memory associated with each shared-memory switch fabric; and

means for sending a packet buffer number and a switch instance for each packet stored by each shared-memory switch fabric to an ingress port controller, the packet buffer number including information indicating where the packet is stored in the shared buffer memory and the switch instance including information indicating which shared-memory switch fabric stored the packet.

82. (new):

The network switch of claim 81, further comprising:

means for removing header or control information from received packets before distribution.

83. (new):

The network switch of claim 81, wherein distributing packets distributes packets from one of the plurality of ports of the crossbar switch fabrics to more than one of the shared-memory switch fabrics is without reference to the final port destination of the packets.

84. (new):

The network switch of claim 81, further comprising:

means for generating forwarding information using the packet buffer number and the switch instance; and

means for sending the forwarding information to an egress port controller via a notify ring.

85. (new):

The network switch of claim 84, further comprising:

means for requesting packets from the shared-memory switch fabrics by an egress port controller using the forwarding information from the ingress port controller; and

means for re-collecting the requested packets from the shared-memory switch fabrics by the egress port controller.

86. (new):

The network switch of claim 85, further comprising:

means for retrieving the requested packets from the shared buffer memory by the shared-memory switch fabrics; and

means for transmitting the packets to a destination network component in an order the packets were received by the ingress port controller.

87. (new):

The network switch of claim 82, further comprising:

means for requesting packets from the shared-memory switch fabrics by an egress port controller based on an availability of a channel regardless of an order the packets were received by an ingress port controller; and

means for re-collecting the requested packets by the egress port controller; and

means for re-ordering the re-collected packets such that packets are to be transmitted to a destination network component in an order the packets were received by the ingress port controller.